

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

Applicant: Sun, Yanling.

Application No: 09/966,204

Filing Date: 09/28/2001

Attorney Docket No: SUN-2  
(S&L P26,643 USA)

Title: Phase-Locked Loop Circuit as well as a  
Voltage-Controlled Oscillator as Used in a  
Phase-Locked Loop Circuit

Art Group: 2819

Examiner: John B. Nguyen

Commissioner for Patents  
Mail Stop Issue Fee  
P.O. Box 1450  
Alexandria, VA 22313-1450

ATTN: OFFICIAL DRAFTSPERSON

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Mail Stop Issue Fee, P.O. Box 1450, Alexandria, VA 22313-1450, ATTN: OFFICIAL DRAFTSPERSON, on Thursday, August 21, 2003.

  
Dorothy M. Musgreave

TRANSMITTAL LETTER TO OFFICIAL DRAFTSPERSON

Dear Official Draftsperson:

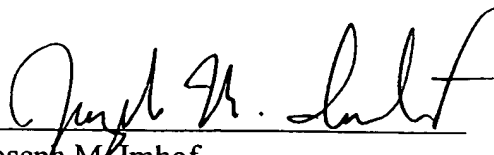
Applicants enclose herewith one (1) sheet of formal drawings (Figures 1, 2, and 3) relating to the above-identified patent application.

The following identifying indicia has been included in the top margin in accordance with 37 CFR §1.84 (c): the title, the inventor's name, and the application number.

Application No. 09/966,204  
Attorney Docket No. P26,643 USA

Respectfully submitted,

Dated: August 21, 2003

  
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Enclosures

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